

3A, 45V Trench Schottky Surface Mount Rectifier

FEATURES

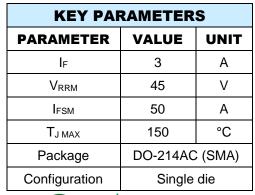
- AEC-Q101 qualified
- Low power loss, high efficiency
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

APPLICATIONS

• Trench Schottky barrier rectifier are designed for high frequency miniature switched mode power supplies such as adapters, lighting

MECHANICAL DATA

- Case: DO-214AC (SMA)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: Indicated by cathode band
- Weight: 0.060g (approximately)







DO-214AC (SMA)



ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)					
PARAMETER	SYMBOL	TSSA3U45H	UNIT		
Marking code on the device		3U45			
Repetitive peak reverse voltage	Vrrm	45	V		
Reverse voltage, total rms value	V _{R(RMS)}	31	V		
Forward current	IF	3	А		
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I _{FSM}	50	А		
Junction temperature	TJ	- 55 to +150	°C		
Storage temperature	T _{STG}	- 55 to +150	°C		



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R _{⊖JL}	10	°C/W	
Junction-to-ambient thermal resistance	Reja	70	°C/W	

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage ⁽¹⁾	I _F = 3A, T _J = 25°C	VF	0.43	0.48	V
	I⊧ = 3A, T」 = 125°C		0.35	0.40	V
Reverse current @ rated $V_R^{(2)}$	$T_J = 25^{\circ}C$	- I _R	-	500	μA
	T _J = 125°C		-	100	mA

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE	PACKAGE	PACKING		
TSSA3U45H	DO-214AC (SMA)	7,500 / Tape & Reel		



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

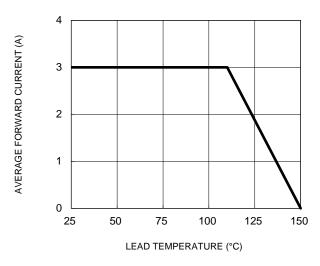
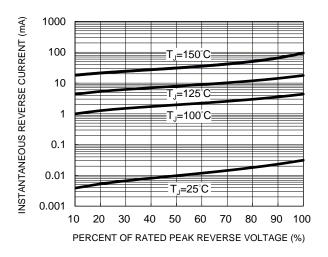


Fig.1 Forward Current Derating Curve

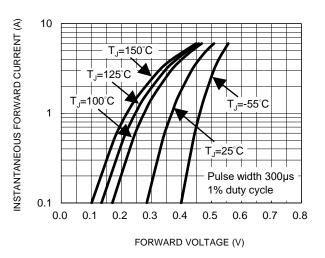
Fig.3 Typical Reverse Characteristics



1000 (1) 100 100 100 (f=1.0MHz Vsig=50mVp-p 0.1 1 10 100 REVERSE VOLTAGE (V)

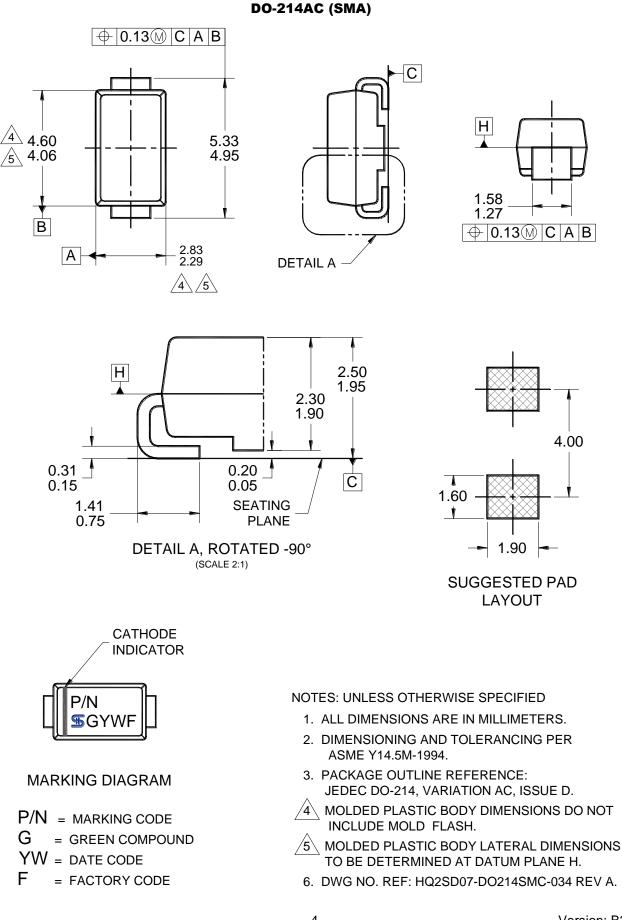
Fig.2 Typical Junction Capacitance







PACKAGE OUTLINE DIMENSIONS





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