

2A, 100V Schottky Barrier Surface Mount Rectifier

FEATURES

- AEC-Q101 qualified
- Low power loss, high efficiency
- Ideal for automated placement
- Guard ring for overvoltage protection
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

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- Switching mode power supply (SMPS)
- Adapters
- Monitor
- TV

MECHANICAL DATA

- Case: DO-214AA (SMB)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- · Polarity: Indicated by cathode band
- Weight: 0.090g (approximately)

KEY PARAMETERS					
PARAMETER	VALUE	UNIT			
l _F	2	Α			
V_{RRM}	100	V			
I _{FSM}	75	Α			
T _J MAX	175	°C			
Package	DO-214AA (SMB)				
Configuration	Single die				









DO-214AA (SMB)



PARAMETER	SYMBOL	SSH210H	UNIT
Marking code on the device		SSH210	
Repetitive peak reverse voltage	V _{RRM}	100	V
Reverse voltage, total rms value	V _R (RMS)	70	V
Forward current	I _F	2	А
Surge peak forward current, 8.3ms single half sine-wave superimposed on rated load	Ifsm	75	А
Junction temperature	TJ	- 55 to +175	°C
Storage temperature	T _{STG}	- 55 to +175	°C



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THERMAL PERFORMANCE				
PARAMETER	SYMBOL	TYP	UNIT	
Junction-to-lead thermal resistance	R _{OJL}	14	°C/W	
Junction-to-ambient thermal resistance	Reja	62	°C/W	
Junction-to-case thermal resistance	Rejc	18	°C/W	

Thermal Performance Note: Units mounted on PCB (10mm x 10mm Cu pad test board)

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
	I _F = 2A, T _J = 25°C		-	0.79	V
Forward voltage ⁽¹⁾	I _F = 2A, T _J = 125°C	V _F	-	0.65	V
Deverse current @ reted V-(2)	T _J = 25°C		-	1	μA
Reverse current @ rated V _R ⁽²⁾	T _J = 125°C	IR	-	1	mA

Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION					
ORDERING CODE	PACKAGE	PACKING			
SSH210H	DO-214AA (SMB)	3,000 / Tape & Reel			



CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

Fig.1 Forward Current Derating Curve

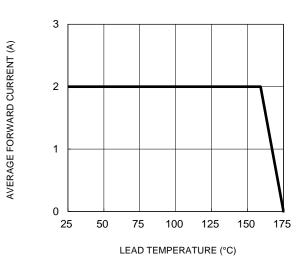


Fig.3 Typical Reverse Characteristics

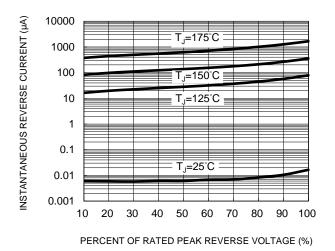


Fig.5 Typical Forward Power Dissipation vs.
Forward Current

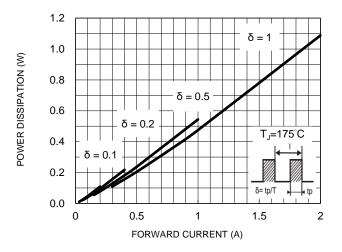


Fig.2 Typical Junction Capacitance

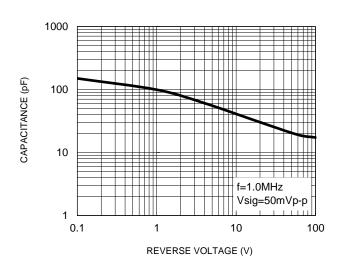
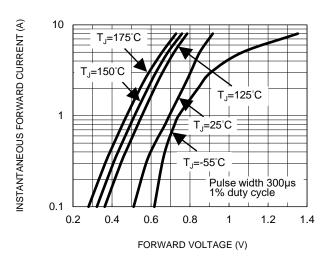


Fig.4 Typical Forward Characteristics

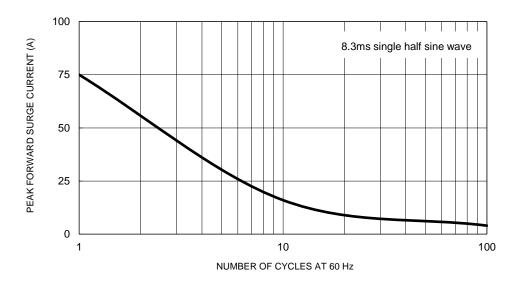




CHARACTERISTICS CURVES

(T_A = 25°C unless otherwise noted)

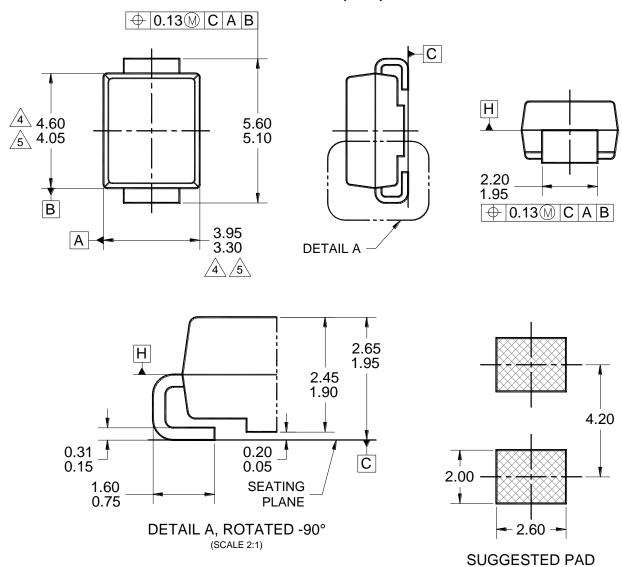
Fig.6 Maximum Non-Repetitive Forward Surge Current

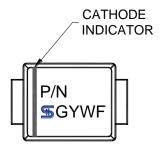




PACKAGE OUTLINE DIMENSIONS

DO-214AA (SMB)





MARKING DIAGRAM

P/N = MARKING CODE

G = GREEN COMPOUND

YW = DATE CODE

F = FACTORY CODE

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.

LAYOUT

- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC DO-214, VARIATION AA, ISSUE D.
- MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH.
- MOLDED PLASTIC BODY LATERAL DIMENSIONS TO BE DETERMINED AT DATUM PLANE H.
- 6. DWG NO. REF: HQ2SD07-DO214SMB-035 REV A.



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