HER101G-K – HER108G-K

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1A, 50V - 1000V High Efficient Rectifier

FEATURES

TAIWAN

Glass passivated chip junction

EMICONDUCTOR

- High current capability, Low $V_{\rm F}$
- High reliability
- High surge current capability
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

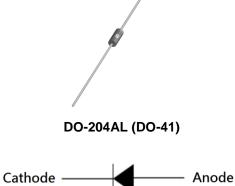
- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

MECHANICAL DATA

- Case: DO-204AL (DO-41)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: Indicated by cathode band
- Weight: 0.330g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I _F	1	А	
V _{RRM}	50 - 1000	V	
I _{FSM}	30	А	
T _{J MAX}	150	°C	
Package	DO-204AL (DO-41)	
Configuration	Single of	die	





ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$ unless otherwise noted)										
PARAMETER	SYMBOL	HER 101 G-K	HER 102 G-K	HER 103 G-K	HER 104 G-K	HER 105 G-K	HER 106 G-K	HER 107 G-K	HER 108 G-K	υνιτ
Marking code on the device		HER 101G	HER 102G	HER 103G	HER 104G	HER 105G	HER 106G	HER 107G	HER 108G	
Repetitive peak reverse voltage	V _{RRM}	50	100	200	300	400	600	800	1000	V
Reverse voltage, total rms value	V _{R(RMS)}	35	70	140	210	280	420	560	700	V
Forward current	١ _F					1				Α
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I _{FSM}	I _{FSM} 30				A				
Junction temperature	TJ				-55 to	+150				°C
Storage temperature	T _{STG}				-55 to	+150				°C





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-ambient thermal resistance	R _{θJA}	60	°C/W	
Junction-to-case thermal resistance	R _{eJC}	15	°C/W	

ELECTRICAL SPECIFICATIONS ($T_A = 25^{\circ}C$ unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
	HER101G-K HER102G-K HER103G-K HER104G-K			-	1.0	V
Forward voltage ⁽¹⁾	HER105G-K	I _F = 1A, T _J = 25°C	V _F	-	1.3	V
	HER106G-K HER107G-K HER108G-K			-	1.7	V
	(2)	$T_J = 25^{\circ}C$		-	5	μA
Reverse current @ rated v	Reverse current @ rated $V_R^{(2)}$		- I _R	-	150	μA
Junction capacitance	HER101G-K HER102G-K HER103G-K HER104G-K HER105G-K	1MHz, V _R = 4.0V	CJ	15	-	pF
	HER106G-K HER107G-K HER108G-K			10	-	pF
Reverse recovery time	HER101G-K HER102G-K HER103G-K HER104G-K HER105G-K		t _{rr}	-	50	ns
	HER106G-K HER107G-K HER108G-K			-	75	ns

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION				
ORDERING CODE ⁽¹⁾	PACKAGE	PACKING		
HER1xG-K	DO-204AL (DO-41)	5,000 / Tape & Reel		
HER1xG-K A0G	DO-204AL (DO-41)	3,000 / Ammo box		

Notes:

1. "x" defines voltage from 50V (HER101G-K) to 1000V (HER108G-K)



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CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

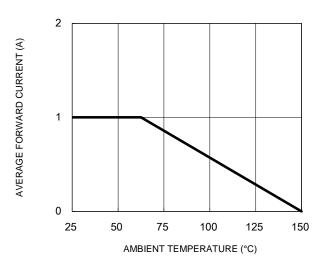
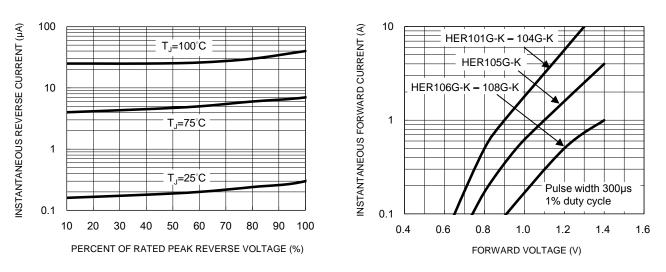


Fig.1 Forward Current Derating Curve

Fig.3 Typical Reverse Characteristics



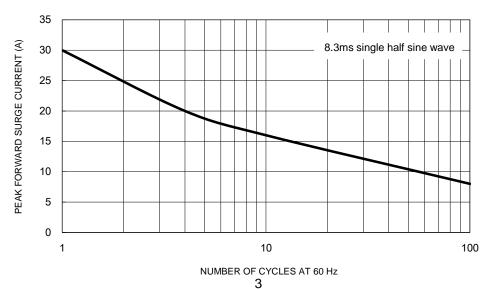


Fig.5 Maximum Non-Repetitive Forward Surge Current

100 HER101G-K - 105G-K

Fig.2 Typical Junction Capacitance

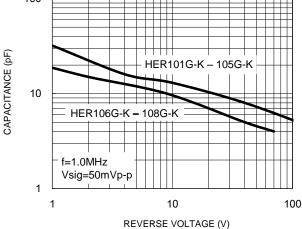


Fig.4 Typical Forward Characteristics



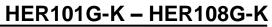
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CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

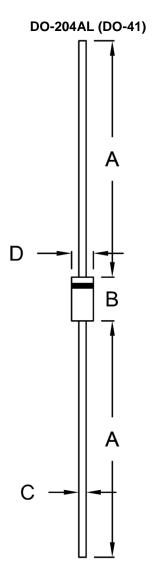
50Ω 10Ω - trr 🗕 NONINDUCTIVE NONINDUCTIVE ~~~ ~~~ +0.5A (-) ± DUT • (+) 50Vdc PULSE 0 GENERATOR = (approx) -0.25A (NOTE 2) (-) IΩ OSCILLOSCOPE 6 (+) (NOTE 1) -1.0A NOTES: 1. Rise Time=7ns max. Input Impedance= ≐ 1 megohm 22pf 2. Rise Time=10ns max. Sourse Impedance= 50 ohms

Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



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PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)		
	Min.	Max.	Min.	Max.	
А	25.40	-	1.000	-	
В	4.20	5.20	0.165	0.205	
С	0.71	0.86	0.028	0.034	
D	2.00	2.70	0.079	0.106	

MARKING DIAGRAM



P/N	= Marking Code
G	= Green Compound
YWW	= Date Code
F	= Factory Code



HER101G-K – HER108G-K

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