

## 200mW, Dual NPN Small Signal Transistor

### FEATURES

- AEC-Q101 qualified
- General-purpose transistors
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

### APPLICATIONS

- General switching and amplification

### MECHANICAL DATA

- Case: SOT-363
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Weight: 6.99mg (approximately)



KEY PARAMETERS		
PARAMETER	VALUE	UNIT
$V_{CBO}$	50	V
$V_{CEO}$	45	V
$V_{EBO}$	6	V
$I_C$	100	mA
$h_{FE}$	630	
Configuration	Dual die	

PACKAGE: SOT-363	PIN CONFIGURATION	CIRCUIT DIAGRAM

ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)			
PARAMETER	SYMBOL	VALUE	UNIT
Power dissipation <sup>(1)</sup>	$P_D$	200	mW
Collector-base voltage	$V_{CBO}$	50	V
Collector-emitter voltage	$V_{CEO}$	45	V
Emitter-base voltage	$V_{EBO}$	6	V
Collector current	$I_C$	100	mA
Junction temperature	$T_J$	-55 to +150	$^\circ\text{C}$
Storage temperature	$T_{STG}$	-55 to +150	$^\circ\text{C}$

#### Note:

1. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint

**THERMAL PERFORMANCE**

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-ambient thermal resistance <sup>(1)</sup>	$R_{\theta JA}$	625	°C/W

**Thermal Performance Note:**

1. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint

**ELECTRICAL SPECIFICATIONS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Collector-base breakdown voltage	$I_C = 10\mu\text{A}$ , $I_E = 0\text{A}$	$V_{(BR)CBO}$	50	-	-	V
Collector-emitter breakdown voltage	$I_C = 10\text{mA}$ , $I_B = 0\text{A}$	$V_{(BR)CEO}$	45	-	-	V
Emitter-base breakdown voltage	$I_E = 10\mu\text{A}$ , $I_C = 0\text{A}$	$V_{(BR)EBO}$	6	-	-	V
Collector-base cut-off current	$V_{CB} = 30\text{V}$ , $I_E = 0\text{A}$	$I_{CBO}$	-	-	15	nA
Emitter-base cut-off current	$V_{EB} = 5\text{V}$ , $I_C = 0\text{A}$	$I_{EBO}$	-	-	0.1	$\mu\text{A}$
DC current gain	$V_{CE} = 5\text{V}$ , $I_C = 2\text{mA}$	$h_{FE}$	110	-	630	-
Collector-emitter saturation voltage	$I_C = 10\text{mA}$ , $I_B = 0.5\text{mA}$	$V_{CE(sat)}$	-	-	0.25	V
	$I_C = 100\text{mA}$ , $I_B = 5\text{mA}$		-	-	0.65	
Base-emitter voltage	$V_{CE} = 5\text{V}$ , $I_C = 2\text{mA}$	$V_{BE}$	580	-	700	mV
Transition frequency	$V_{CE} = 5\text{V}$ , $I_C = 10\text{mA}$ , $f = 100\text{MHz}$	$f_T$	-	200	-	MHz
Output capacitance	$V_{CB} = 10\text{V}$ , $I_E = 0\text{A}$ , $f = 1\text{MHz}$	$C_{obo}$	-	1.5	-	pF

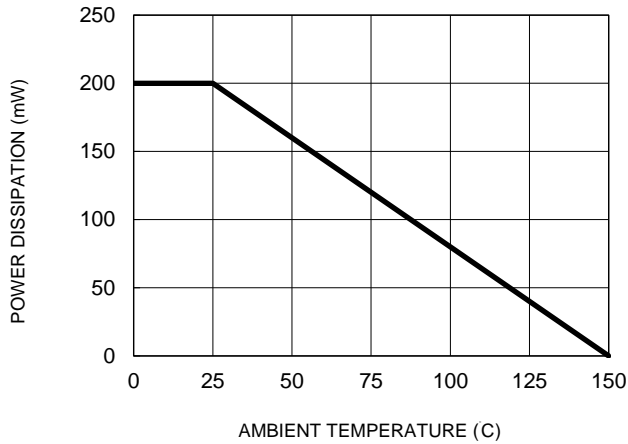
**ORDERING AND MARKING INFORMATION**

ORDERING CODE	PACKAGE	PACKING
BC847SH RFG	SOT-363	3,000 / 7" Tape & Reel

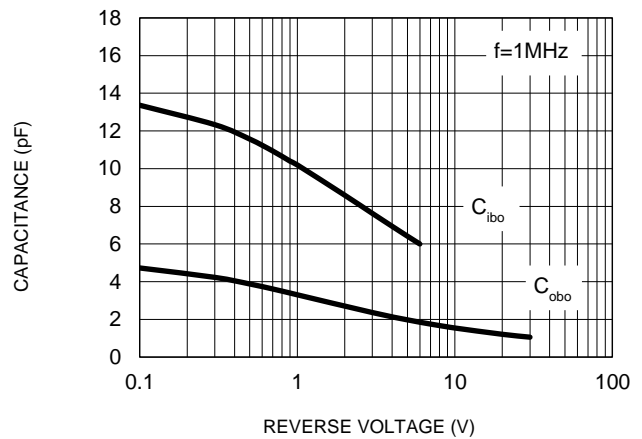
## CHARACTERISTICS CURVES

( $T_A = 25^\circ\text{C}$  unless otherwise noted)

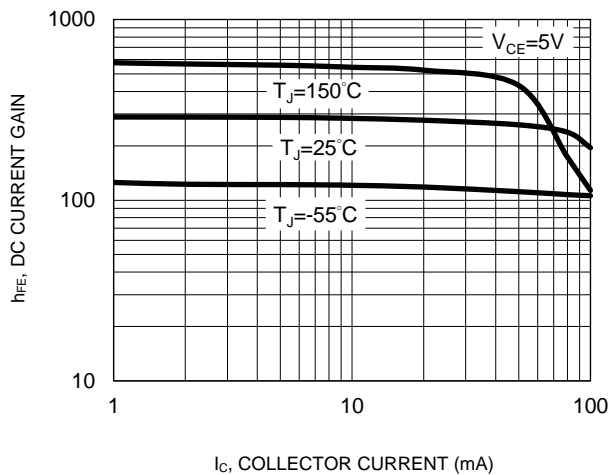
**Fig.1 Power Dissipation Curve**



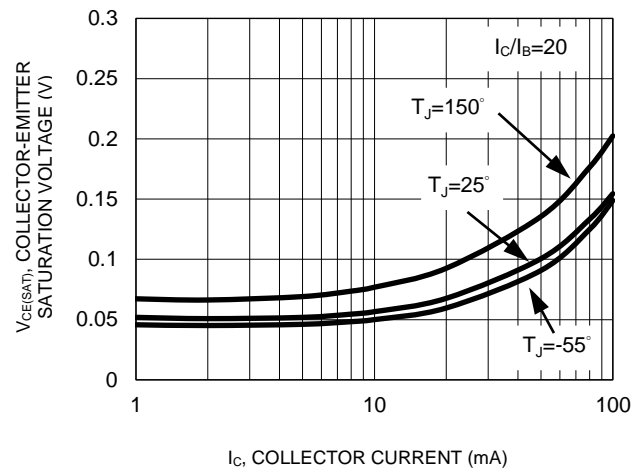
**Fig.2 Typical Capacitance Characteristics**



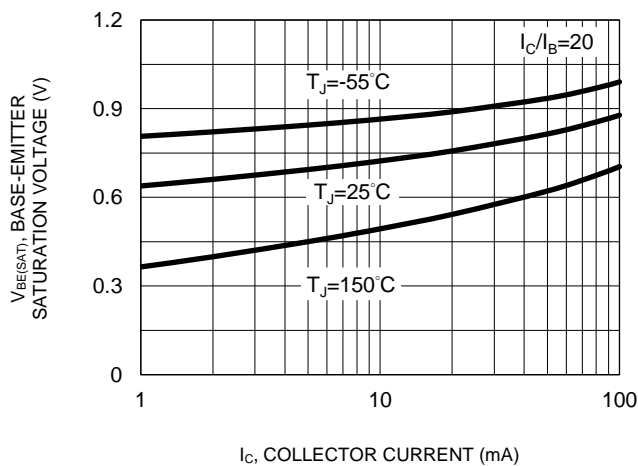
**Fig.3 DC Current Gain vs. Collector Current**



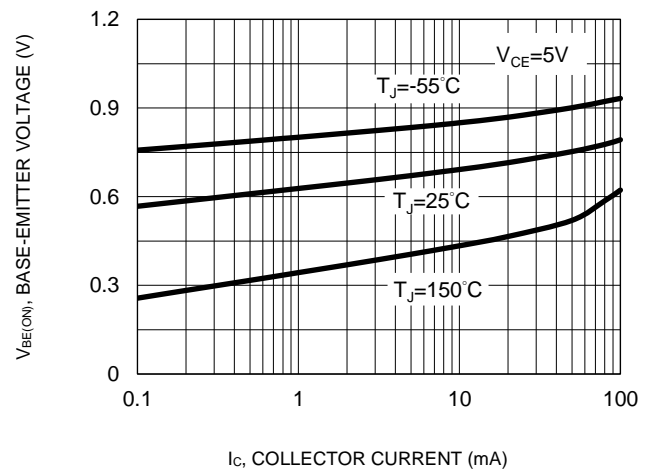
**Fig.4 Collector-Emitter Saturation Voltage vs. Collector Current**



**Fig.5 Base-Emitter Saturation Voltage vs. Collector Current**

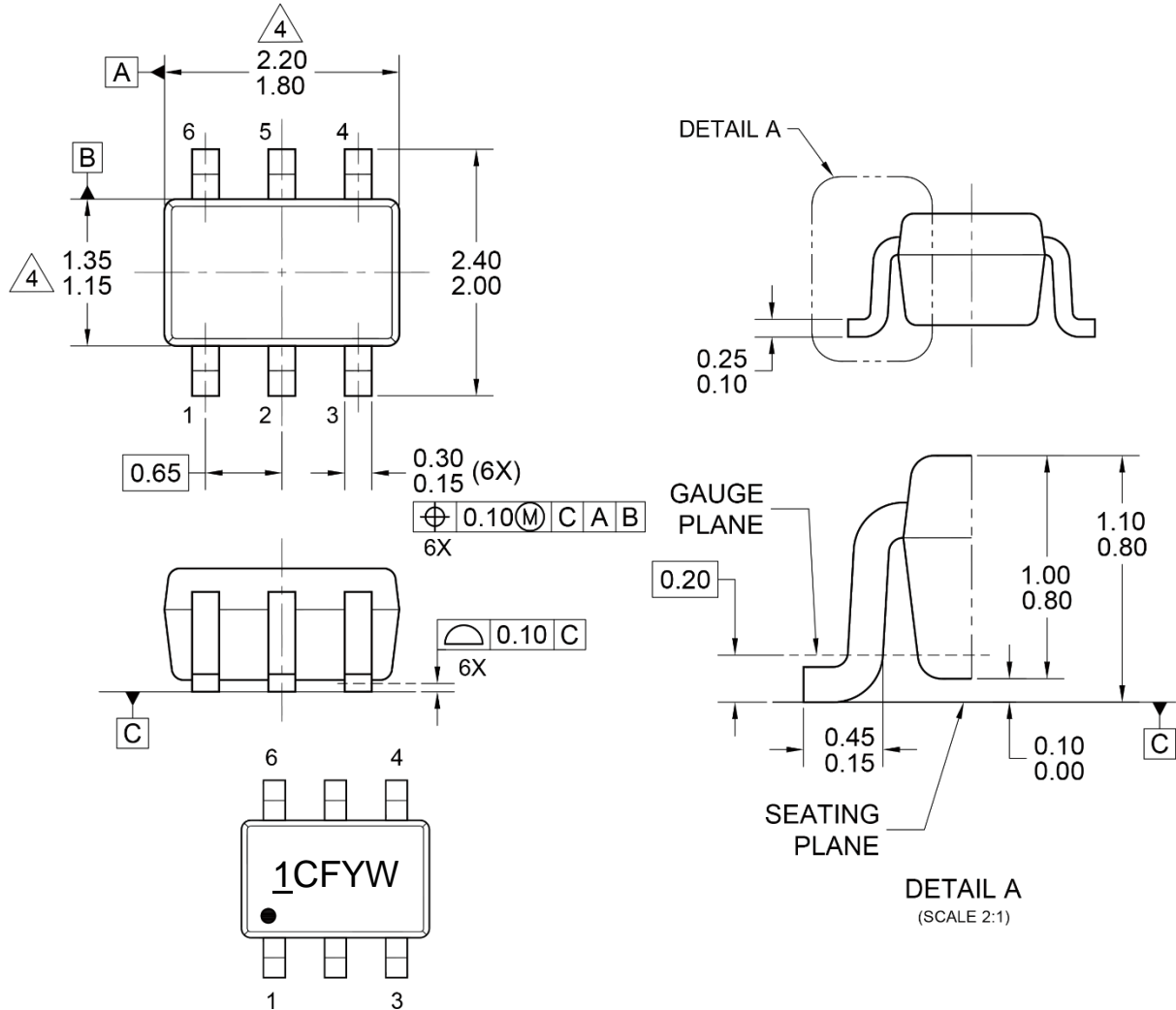


**Fig.6 Base-Emitter Voltage vs. Collector Current**



## PACKAGE OUTLINE DIMENSIONS

### SOT-363

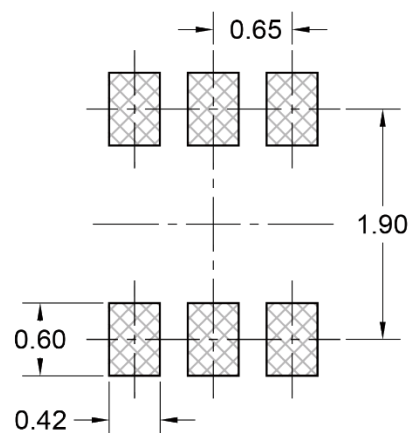


### MARKING DIAGRAM

- 1C = Device marking  
 F = Factory code  
 Y = Year code  
 W = Bi-Week code (A~Z)

### NOTES: UNLESS OTHERWISE SPECIFIED

- ALL DIMENSIONS ARE IN MILLIMETERS.
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- PACKAGE OUTLINE REFERENCE: JEITA ED-7500A, EIAJ SC-88.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- DWG NO. REF: HQ2SD07-SOT363-097 REV B.



### SUGGESTED PAD LAYOUT

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