

215mA, 100V Dual High Speed Switching Diode

FEATURES

- AEC-Q101 qualified
- Fast switching speed
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

APPLICATIONS

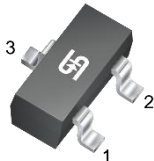
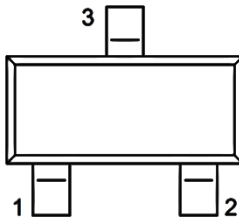
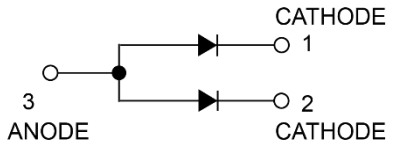
- High-speed switching
- General-purpose switching

MECHANICAL DATA

- Case: SOT-23
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Weight: 8.00mg (approximately)

KEY PARAMETERS		
PARAMETER	VALUE	UNIT
I_F	215	mA
V_{RRM}	100	V
I_{FSM}	2	A
$T_{J\ MAX}$	150	°C
V_F at $I_F=150mA$	1.25	V
Configuration	Dual die	



PACKAGE: SOT-23	PIN CONFIGURATION	CIRCUIT DIAGRAM
		

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)				
PARAMETER		SYMBOL	VALUE	UNIT
Power dissipation ⁽¹⁾		P_D	350	mW
Repetitive peak reverse voltage		V_{RRM}	100	V
Forward current		I_F	215	mA
Repetitive peak forward current		I_{FRM}	300	mA
Non-repetitive peak forward surge current	$t = 1s$	I_{FSM}	1	A
	$t = 1\mu s$		2	A
Junction temperature		T_J	-55 to +150	°C
Storage temperature		T_{STG}	-55 to +150	°C

Note:

1. Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint

THERMAL PERFORMANCE

PARAMETER	SYMBOL	TYP	UNIT
Junction-to-ambient thermal resistance ⁽¹⁾	$R_{\theta JA}$	357	°C/W

Thermal Performance Note:

- Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint

ELECTRICAL SPECIFICATIONS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	CONDITIONS	SYMBOL	MIN	TYP	MAX	UNIT
Forward voltage ⁽¹⁾	$I_F = 1\text{mA}$	V_F	-	-	0.715	V
	$I_F = 10\text{mA}$		-	-	0.855	
	$I_F = 50\text{mA}$		-	-	1.000	
	$I_F = 150\text{mA}$		-	-	1.250	
Reverse breakdown voltage	$I_R = 100\mu\text{A}$	V_{BR}	100	-	-	V
Reverse current ⁽²⁾	$V_R = 75\text{V}$	I_R	-	-	0.5	μA
	$V_R = 75\text{V}, T_J = 150^\circ\text{C}$		-	-	50	μA
Junction capacitance	$f = 1\text{MHz}, V_R = 0\text{V}$	C_J	-	-	2	pF
Reverse recovery time	$I_F = I_R = 10\text{mA},$ $I_{RR} = 1\text{mA}, R_L = 100\Omega$	t_{rr}	-	-	4	ns

Notes:

- Pulse test with $PW=0.3\text{ ms}$
- Pulse test with $PW=30\text{ ms}$

ORDERING INFORMATION

ORDERING CODE	PACKAGE	PACKING
BAW56H RFG	SOT-23	3,000 / 7" Tape & Reel

CHARACTERISTICS CURVES

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Fig.1 Power Dissipation Curve

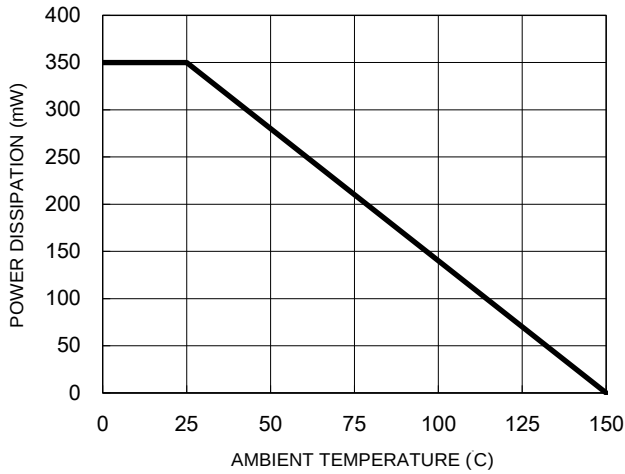


Fig.2 Typical Junction Capacitance

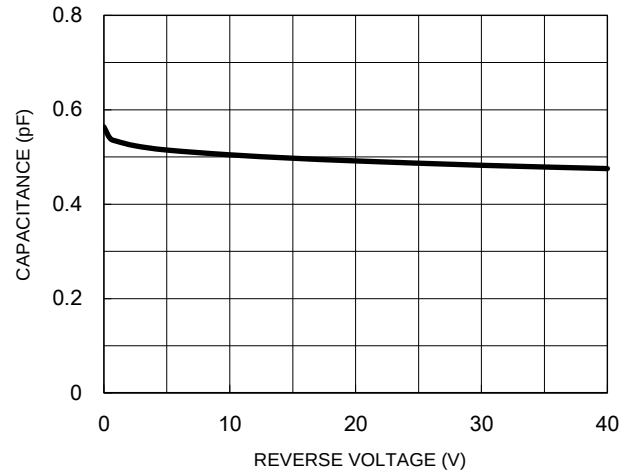


Fig.3 Typical Reverse Characteristics

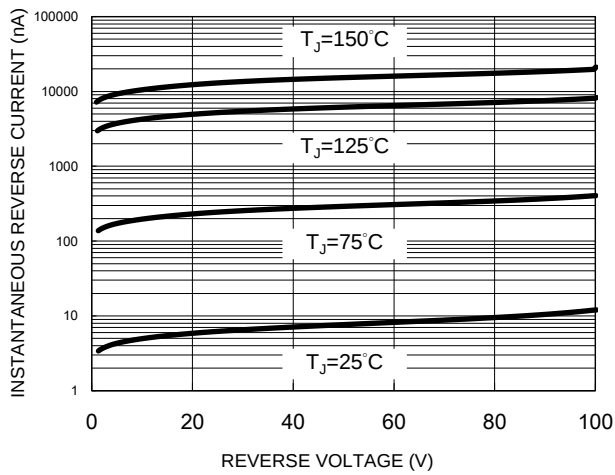
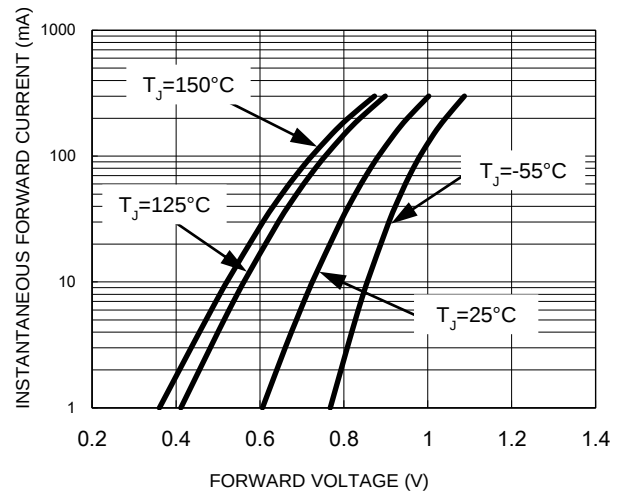
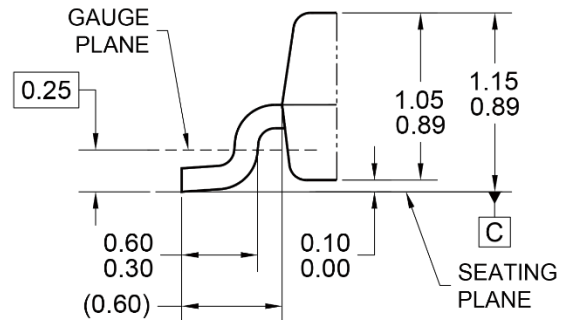
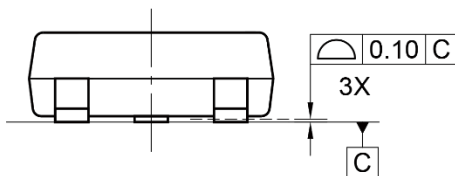
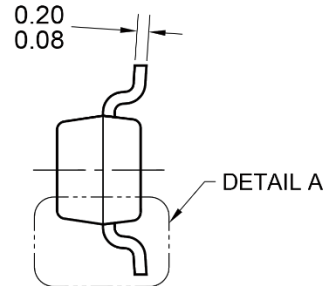
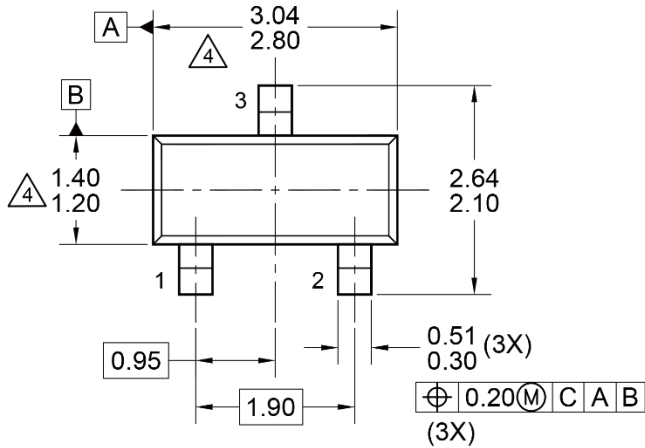


Fig.4 Typical Forward Characteristics

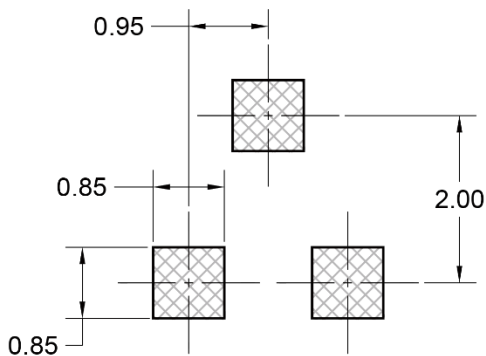


PACKAGE OUTLINE DIMENSIONS

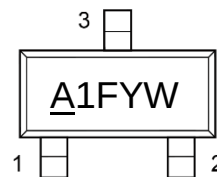
SOT-23



DETAIL A, ROTATED -90°
(SCALE 2:1)



SUGGESTED PAD LAYOUT



MARKING DIAGRAM

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
3. PACKAGE OUTLINE REFERENCE: JEDEC TO-236, ISSUE H, VARIATION AA.

4. MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

5. DWG NO. REF: HQ2SD07-SOT23JEDEC-104 REV B.

A1 = Device marking
F = Factory code
Y = Year code
W = Bi-Week code (A~Z)

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